

### REMARKS

Claims 1-16, 20, 21, 26-72, 75, 76, 79, 80, 84, 85, 90 and 91 are pending in the application, with claims 1-4, 8, 9, 20 and 21 being independent. Claims 1 and 2 have been amended, and claims 17-19, 22-25, 73, 74, 77, 78, 81-83, 86-89 and 92-94 have been canceled.

Applicant acknowledges with appreciation the Examiner's allowance of claims 3-5, 8-13, 27-29, 31-33, 35-37, 40-45, 47-49, 52-57, 60-62 and 65-67, and the Examiner's indication that claims 6, 7, 26, 30, 38, 39, 50, 51, 63 and 64 are directed to allowable subject matter.

The drawings have been amended in response to the Examiner's objection. No new matter has been introduced.

Claims 1, 2, 16, 20, 21, 59, 71, 72, 75 and 76 have been rejected as being anticipated by Okumura.

Claim 1 has been amended to recite at least first and second memories, the operations of which are controlled by at least first and second gate signal lines. This arrangement is illustrated in Fig. 24 of the application. Applicant requests reconsideration and withdrawal of the rejection of claim 1 and its dependent claims at least because Okumura does not describe or suggest first and second memories controlled in the manner recited in claim 1.

Claim 2 has been amended to recite at least first and second memories connected to first and second source signal lines. This arrangement is illustrated in Fig. 6 of the application. Applicant requests reconsideration and withdrawal of the rejection of claim 2 and its dependent claims at least because Okumura does not describe or suggest first and second memories arranged in the manner recited in claim 2.

Each of claims 20 and 21 recites, among other features, repeatedly reading digital signals stored in memory circuits included in a pixel and converting the digital signals into corresponding analog signals. Applicant requests reconsideration and withdrawal of this rejection because Okumura does not describe or suggest converting a digital signal read from memory into an analog signal. Instead, Okumura, as shown in Fig. 27, combines the signals from two memories using an OR gate, and then converts the combined signal into an analog signal.

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Amendments to the Drawings:

The attached 8 annotated sheets of drawings include changes to Figs. 1, 2, 13-16, 26, 29A and 29B.

In Figs. 1, 2 and 26, descriptive labels have been added.

In Figs. 13, 15 and 16, the designation “(PRIOR ART)” and descriptive labels have been added.

In Figs. 14, 29A and 29B, the designation “(PRIOR ART)” has been added.

Attachments following last page of this Amendment:

Annotated Sheet Showing Change(s) (8 pages)

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Claims 14, 34, 79 and 80 have been rejected as being unpatentable over Okumura. Applicant requests reconsideration and withdrawal of this rejection at least for the reasons discussed above with respect to the independent claims.

Claims 15, 46, 58, 84 and 85 have been rejected as being unpatentable over Okumura in view of Sato. Applicant requests reconsideration and withdrawal of this rejection at least because Sato does not remedy the failure of Okumura to describe or suggest the subject matter of the independent claims.


Applicant submits that all claims are in condition for allowance.

A check for \$300 in payment of the extension fee (\$120) and the late submission fee of §1.17(p) (\$180) is enclosed. Please apply any other charges or credits to deposit account 06-1050.

Respectfully submitted,

Date: \_\_\_\_\_

1/10/05

  
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